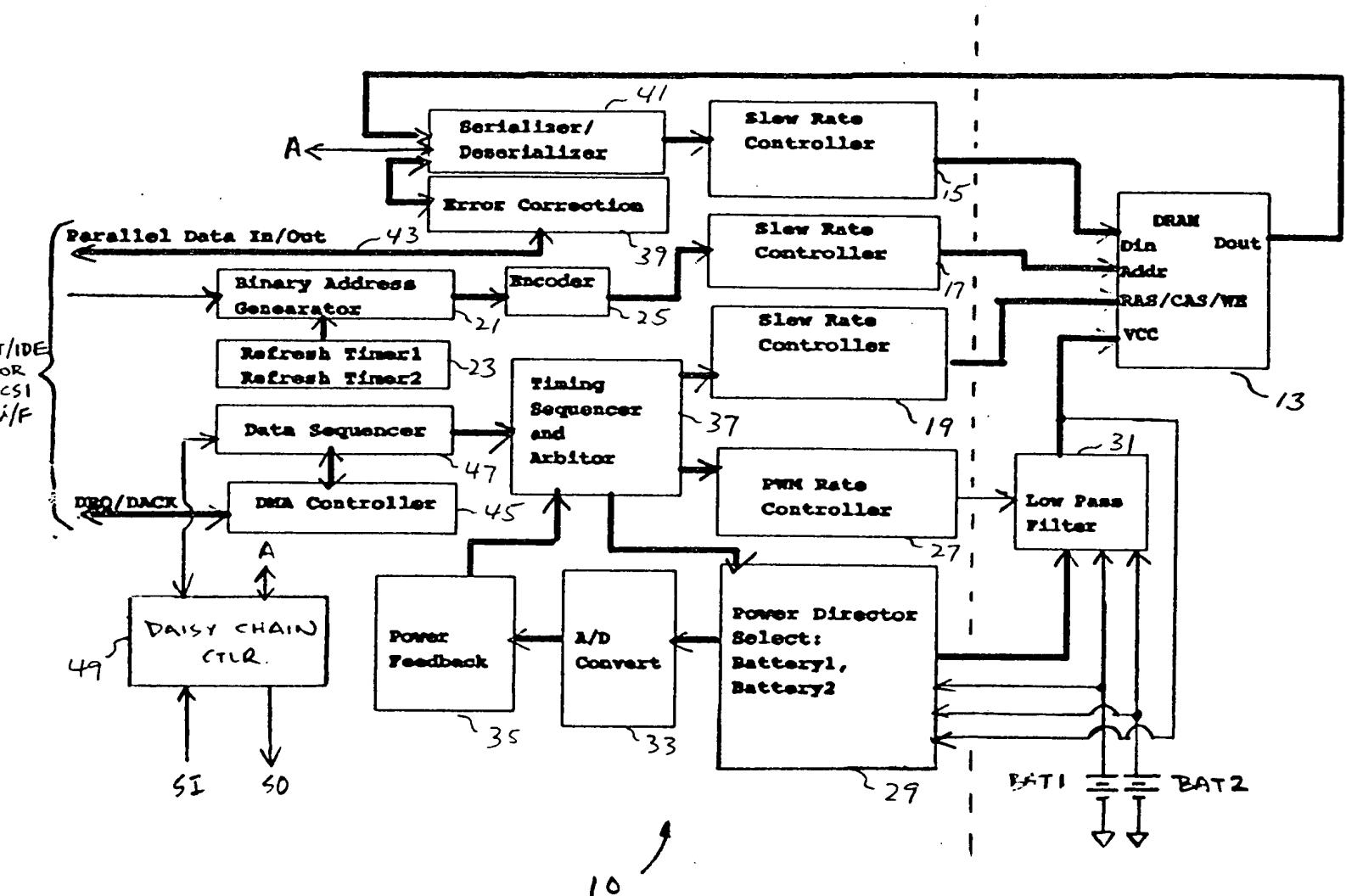


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Figure 1



10

# Power Sequence

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RAM VCC

+5V  
+3V  
+2V  
+1.5V

Fig 2a

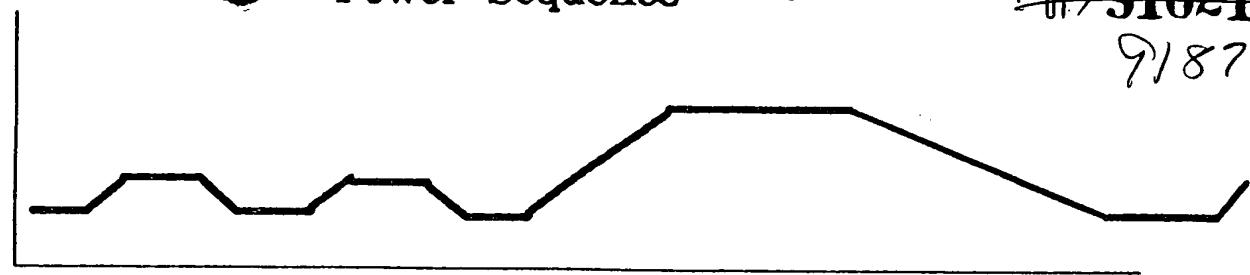


Fig 2b  
Cycle

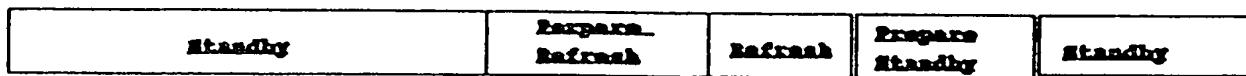
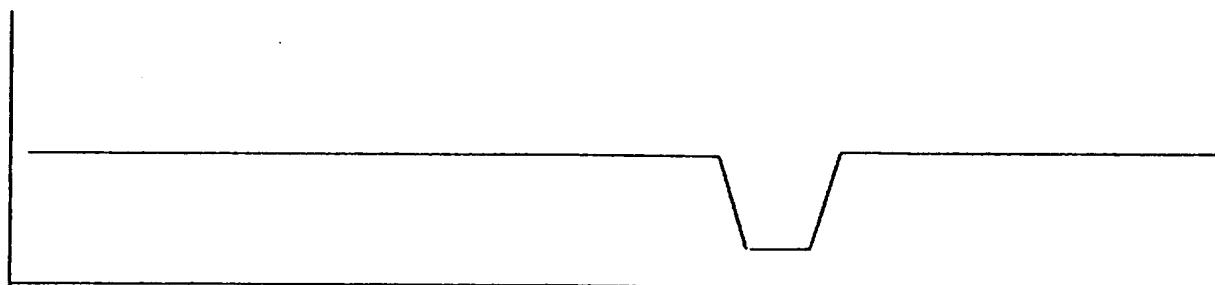


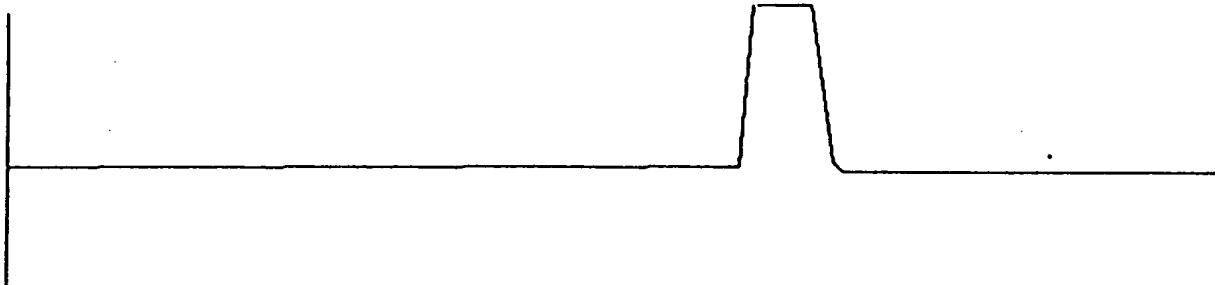
Fig 2c

+2.7V



Current  
(mA)

1000  
100  
10



Clustering Memory Nodes

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Dynamic Power Module

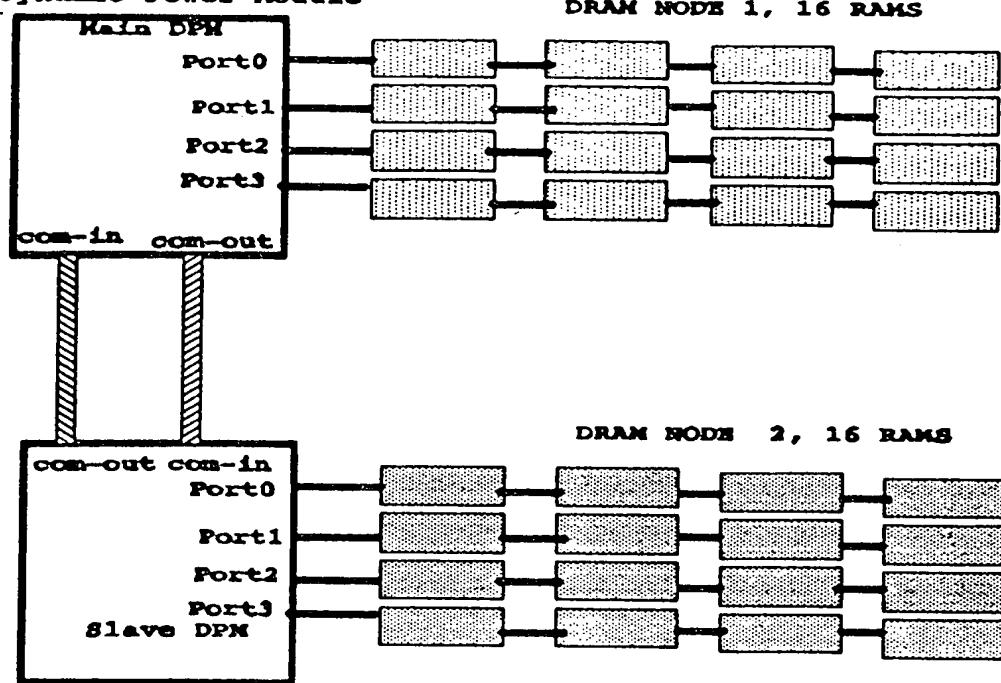


Fig 3

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## Voltage across DRAMS For Four DRAMs

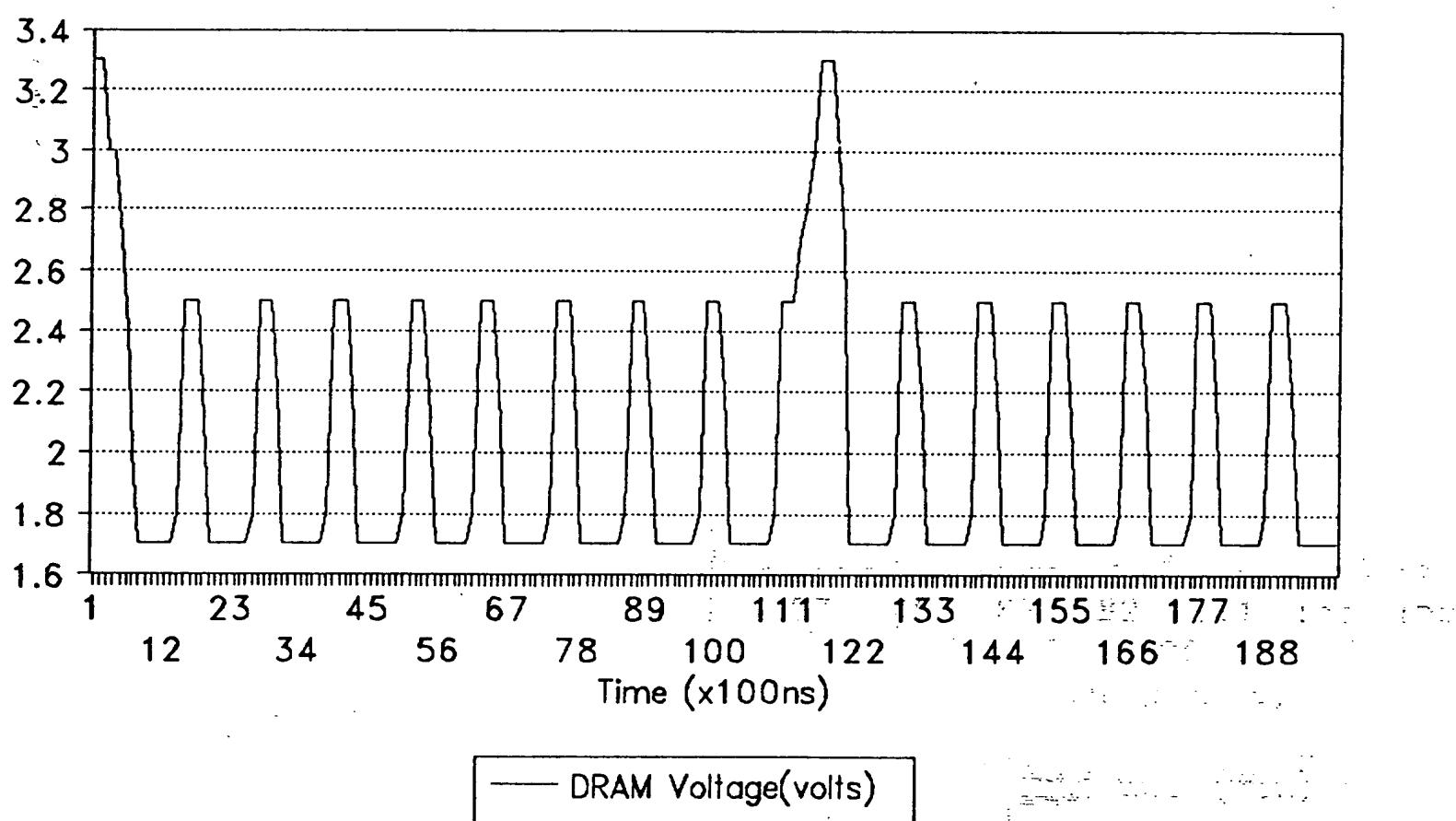


Fig 4

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M/910213  
918745

## Power for Four Rams over one Period (Refresh access is included)

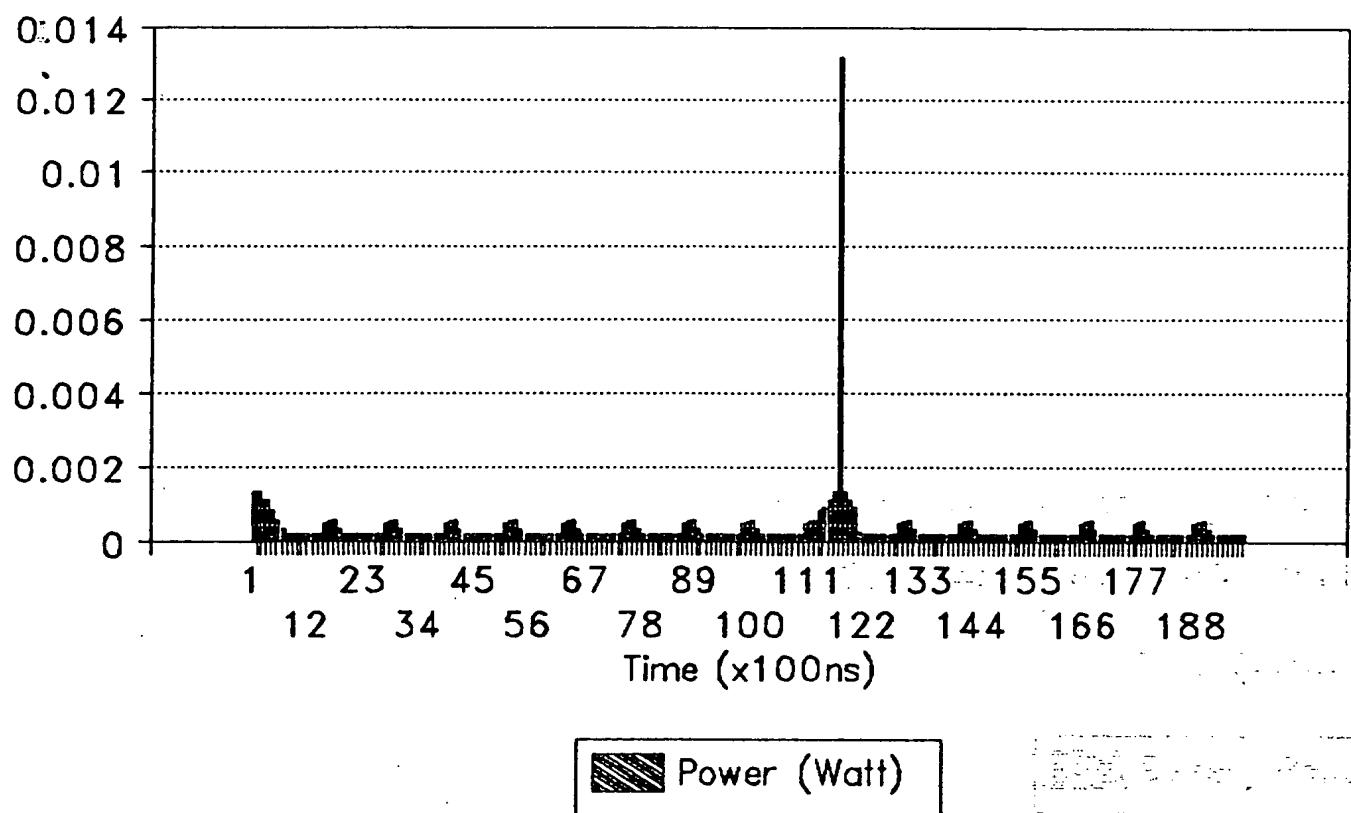


Fig 5

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## Battery Power Consumed For Four DRAMs

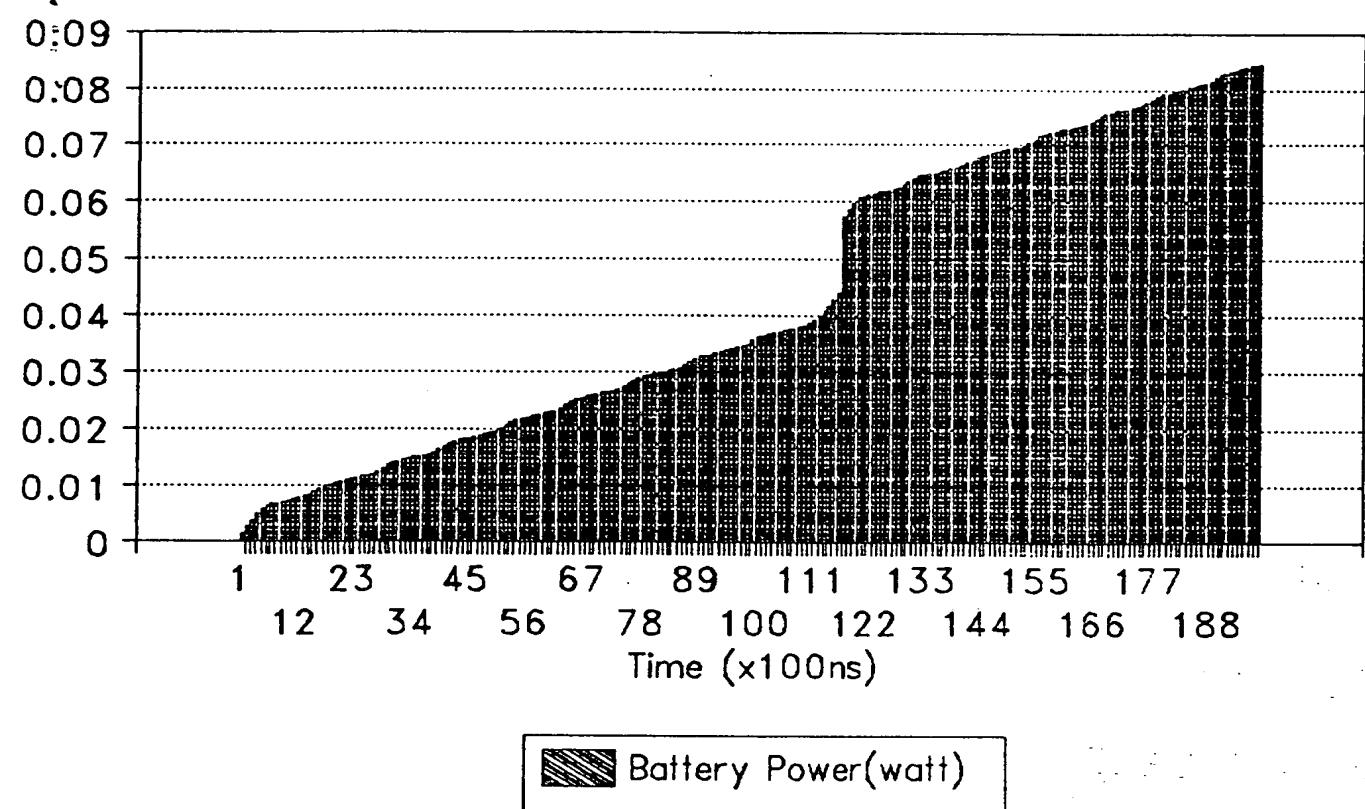


Fig 6

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## Battery Life(50mAhr), 8 DRAMS (Hot Standby Battery)

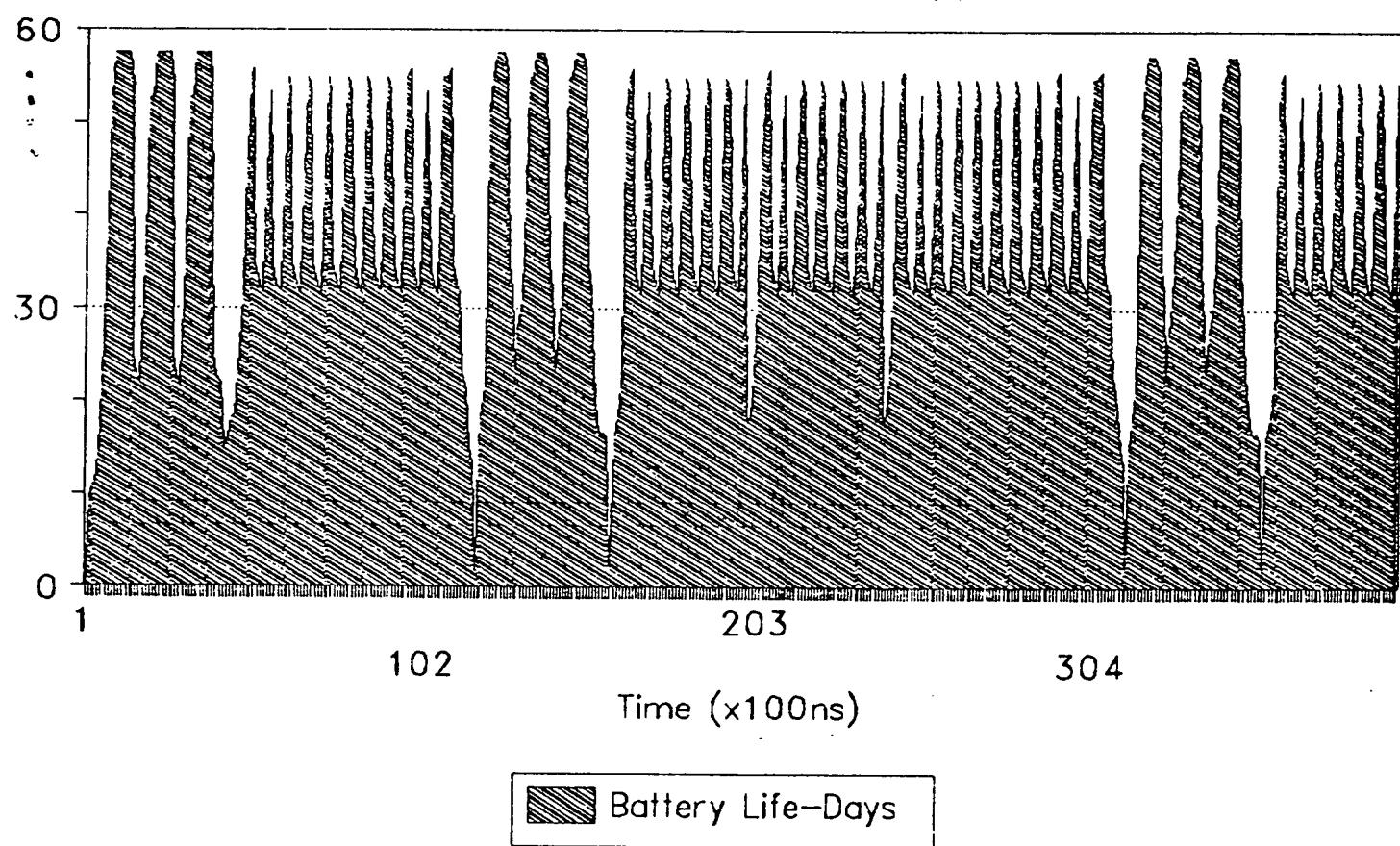


Fig 7